SDAS053B - APRIL 1982 - REVISED JANUARY 1995

- 'AS1034A Offer High Capacitive-Drive Capability
- Noninverting Drivers
- Package Options Include Plastic Small-Outline (D) Packages, Ceramic Chip Carriers (FK), and Standard Plastic (N) and Ceramic (J) 300-mil DIPs

#### description

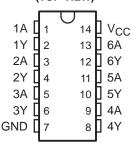
These devices contain six independent noninverting drivers. They perform the Boolean function Y = A.

The SN54ALS1034 and SN54AS1034A are characterized for operation over the full military temperature range of -55°C to 125°C. The SN74ALS1034 and SN74AS1034A are characterized for operation from 0°C to 70°C.

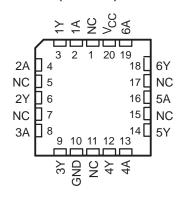
FUNCTION TABLE (each buffer)

INPUT A	OUTPUT Y
Н	Н
L	L

#### SN54ALS1034, SN54AS1034A . . . J PACKAGE SN74ALS1034, SN74AS1034A . . . D OR N PACKAGE (TOP VIEW)



## SN54ALS1034, SN54AS1034A . . . FK PACKAGE (TOP VIEW)



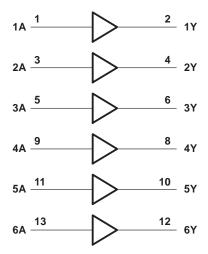
NC - No internal connection

### logic symbol†

4.4	1	 2	1Y
1A	3	4	
2A 3A	5	6	2Y
	9	8	3Y 4Y
4A	11	10	
5A	13	12	5Y
6A			6Y

<sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

### logic diagram (positive logic)



Pin numbers shown are for the D, J, and N packages.

## SN54ALS1034, SN54AS1034A, SN74ALS1034, SN74AS1034A HEX DRIVERS

SDAS053B - APRIL 1982 - REVISED JANUARY 1995

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V <sub>CC</sub>	
Input voltage, V <sub>I</sub>	
Operating free-air temperature range, TA: SN54ALS1034	₊
SN74ALS1034	4 0°C to 70°C
Storage temperature range	–65°C to 150°C

#### recommended operating conditions

		SN	SN54ALS1034			SN74ALS1034			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V	
VIH	High-level input voltage	2			2			V	
V <sub>IL</sub>	Low-level input voltage			0.7			0.8	V	
ІОН	High-level output current			-12			-15	mA	
lOL	Low-level output current			12			24	mA	
T <sub>A</sub>	Operating free-air temperature	-55		125	0		70	°C	

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

BABAMETER	TEGE GOVERNO		SN5	4ALS10	)34	SN7	4ALS10	34		
PARAMETER	TEST CO	ONDITIONS	MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT	
VIK	$V_{CC} = 4.5 \text{ V},$	$I_{I} = -18 \text{ mA}$			-1.2			-1.2	V	
	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -0.4 \text{ mA}$	V <sub>CC</sub> -2			V <sub>CC</sub> -2	<u>!</u>			
V		$I_{OH} = -3 \text{ mA}$	2.4	3.2		2.4	3.2		.,	
Voн	V <sub>CC</sub> = 4.5 V	$I_{OH} = -12 \text{ mA}$	2						V	
		$I_{OH} = -15 \text{ mA}$				2				
Va	V <sub>CC</sub> = 4.5 V	$I_{OL} = 12 \text{ mA}$		0.25	0.4				V	
VoL		$I_{OL} = 24 \text{ mA}$					0.35	0.5	V	
lį	$V_{CC} = 5.5 V,$	V <sub>I</sub> = 7 V			0.1			0.1	mA	
lіН	$V_{CC} = 5.5 V,$	V <sub>I</sub> = 2.7 V			20			20	μΑ	
I <sub>IL</sub>	$V_{CC} = 5.5 V,$	V <sub>I</sub> = 0.4 V			-0.1			-0.1	mA	
IO§	$V_{CC} = 5.5 V,$	$V_0 = 2.25 \text{ V}$	-20		-112	-30		-112	mA	
Iссн	$V_{CC} = 5.5 V,$	V <sub>I</sub> = 4.5 V		3	6		3	6	mA	
ICCL	$V_{CC} = 5.5 V,$	V <sub>I</sub> = 0		8	14		8	14	mA	

<sup>‡</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ .

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

<sup>§</sup> The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

## SN54ALS1034, SN54AS1034A, SN74ALS1034, SN74AS1034A HEX DRIVERS

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#### switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC}$ = 4.5 V to 5.5 V, $C_L$ = 50 pF, $R_L$ = 500 Ω, $T_A$ = MIN to MAX <sup>†</sup> SN54ALS1034 SN74ALS1034				
			SN54AL	.S1034	SN74AL	.S1034	
			MIN	MAX	MIN	MAX	
<sup>t</sup> PLH	٨	V	1	11	1	8	ns
t <sub>PHL</sub>	А	1	1	13	1	8	110

T For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage, V <sub>CC</sub>		 7 V
Input voltage, V <sub>I</sub>		 7 V
Operating free-air temperature range, T <sub>A</sub> :	SN54AS1034A	 -55°C to 125°C
	SN74AS1034A	 0°C to 70°C
Storage temperature range		 -65°C to 150°C

<sup>‡</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

### recommended operating conditions§

		SN54AS1034A			SN7	LINUT		
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
$V_{IL}$	Low-level input voltage			8.0			8.0	V
IOH	High-level output current			-40			-48	mA
loL	Low-level output current			40			48	mA
TA	Operating free-air temperature	-55		125	0		70	°C

<sup>§</sup> These high sink- or source-current devices are not recommended for use above 40 MHz.

## SN54ALS1034, SN54AS1034A, SN74ALS1034, SN74AS1034A HEX DRIVERS

SDAS053B - APRIL 1982 - REVISED JANUARY 1995

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

		TEST SOUDITIONS		4AS103	4A	SN7	'4AS103	4A	
PARAMETER	TEST CO	ONDITIONS	MIN	TYP <sup>†</sup>	MAX	MIN	TYP†	MAX	UNIT
VIK	$V_{CC} = 4.5 V,$	I <sub>I</sub> = -18 mA			-1.2			-1.2	V
	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -2 \text{ mA}$	V <sub>CC</sub> -2	2		V <sub>CC</sub> -2	)		
V		$I_{OH} = -3 \text{ mA}$	2.4	3.2		2.4	3.2		V
VOH	V <sub>CC</sub> = 4.5 V	$I_{OH} = -40 \text{ mA}$	2						V
		$I_{OH} = -48 \text{ mA}$				2			
V	\\ \( \( \int \) \\	$I_{OL} = 40 \text{ mA}$		0.25	0.5				V
VOL	V <sub>CC</sub> = 4.5 V	$I_{OL} = 48 \text{ mA}$					0.35	0.5	V
lį	$V_{CC} = 5.5 V$ ,	$V_I = 7 V$			0.1			0.1	mA
lіН	$V_{CC} = 5.5 V$ ,	$V_{ } = 2.7 V$			20			20	μΑ
I <sub>IL</sub>	$V_{CC} = 5.5 V,$	V <sub>I</sub> = 0.4 V			-0.5			-0.5	mA
1 <sub>0</sub> ‡	V <sub>CC</sub> = 5.5 V,	V <sub>O</sub> = 2.25 V	-50		-200	-50		-200	mA
Іссн	V <sub>CC</sub> = 5.5 V,	V <sub>I</sub> = 4.5 V		9	15		9	15	mA
ICCL	V <sub>CC</sub> = 5.5 V,	V <sub>I</sub> = 0		21	35		21	35	mA

 $<sup>\</sup>frac{1}{1}$  All typical values are at  $V_{CC} = 5$  V,  $T_A = 25$ °C.

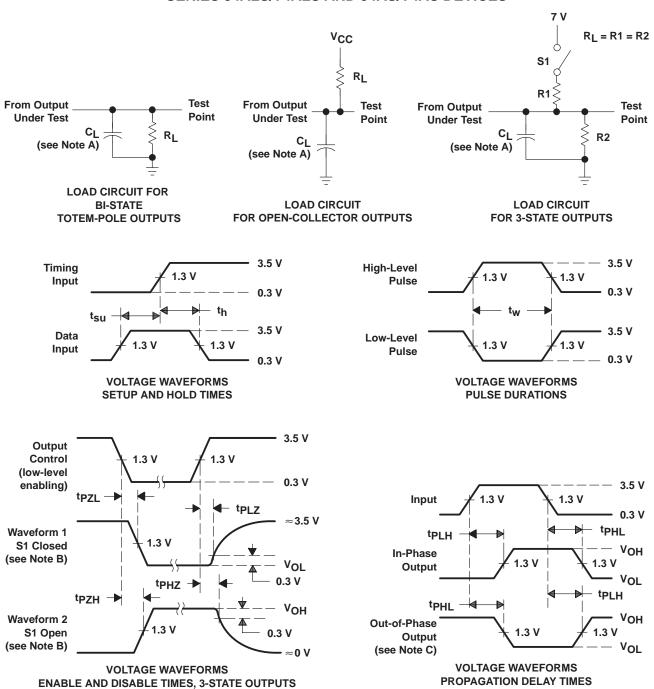
#### switching characteristics (see Figure 1)

PARAMETER	TER FROM TO (OUTPUT)			= 50 pF, = 500 Ω = MIN to	, o MAX§			
			SN54AS	1034A	SN74AS	1034A		
			MIN	MAX	MIN	MAX		
<sup>t</sup> PLH	A	V	1	6.5	1	6	20	
<sup>t</sup> PHL	] ^	l l	1	6.5	1	6	ns	

 $<sup>\</sup>S$  For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

<sup>&</sup>lt;sup>‡</sup> The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I<sub>OS</sub>.

#### PARAMETER MEASUREMENT INFORMATION SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



NOTES: A. C<sub>I</sub> includes probe and jig capacitance.

- Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. When measuring propagation delay items of 3-state outputs, switch S1 is open.
- D. All input pulses have the following characteristics:  $PRR \le 1$  MHz,  $t_r = t_f = 2$  ns, duty cycle = 50%.
- E. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms





#### **PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
5962-88731012A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
5962-8873101CA	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
5962-8873101DA	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type
84031012A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
8403101CA	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
8403101DA	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type
JM38510/38411B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
JM38510/38411BCA	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SN54ALS1034J	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SN54AS1034AJ	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SN74ALS1034D	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74ALS1034DE4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74ALS1034DG4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74ALS1034DR	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74ALS1034DRE4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74ALS1034DRG4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74ALS1034N	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74ALS1034NE4	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74ALS1034NSR	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74ALS1034NSRE4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74ALS1034NSRG4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74AS1034AD	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74AS1034ADE4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74AS1034ADG4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74AS1034ADR	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74AS1034ADRE4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74AS1034ADRG4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74AS1034AN	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74AS1034ANE4	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type



#### PACKAGE OPTION ADDENDUM

18-Sep-2008

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
SN74AS1034ANSR	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74AS1034ANSRE4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74AS1034ANSRG4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SNJ54ALS1034FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
SNJ54ALS1034J	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SNJ54ALS1034W	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type
SNJ54AS1034AFK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
SNJ54AS1034AJ	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SNJ54AS1034AW	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND**: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

**Pb-Free** (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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#### TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



#### \*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74ALS1034DR	SOIC	D	14	2500	330.0	16.4	6.5	9.0	2.1	8.0	16.0	Q1
SN74ALS1034NSR	SO	NS	14	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1
SN74AS1034ADR	SOIC	D	14	2500	330.0	16.4	6.5	9.0	2.1	8.0	16.0	Q1
SN74AS1034ANSR	SO	NS	14	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1





\*All dimensions are nominal

All difficiliate de fiornifici							
Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74ALS1034DR	SOIC	D	14	2500	346.0	346.0	33.0
SN74ALS1034NSR	SO	NS	14	2000	346.0	346.0	33.0
SN74AS1034ADR	SOIC	D	14	2500	346.0	346.0	33.0
SN74AS1034ANSR	SO	NS	14	2000	346.0	346.0	33.0

#### FK (S-CQCC-N\*\*)

#### **28 TERMINAL SHOWN**

#### **LEADLESS CERAMIC CHIP CARRIER**



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a metal lid.
- D. The terminals are gold plated.
- E. Falls within JEDEC MS-004



#### **MECHANICAL DATA**

## NS (R-PDSO-G\*\*)

## 14-PINS SHOWN

#### PLASTIC SMALL-OUTLINE PACKAGE



- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



## 14 LEADS SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package is hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
- E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

## W (R-GDFP-F14)

## CERAMIC DUAL FLATPACK



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only.
- E. Falls within MIL STD 1835 GDFP1-F14 and JEDEC MO-092AB



## D (R-PDSO-G14)

#### PLASTIC SMALL-OUTLINE PACKAGE



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.
- Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.
- E. Reference JEDEC MS-012 variation AB.



## N (R-PDIP-T\*\*)

## PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- The 20 pin end lead shoulder width is a vendor option, either half or full width.



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